

Fig. 1

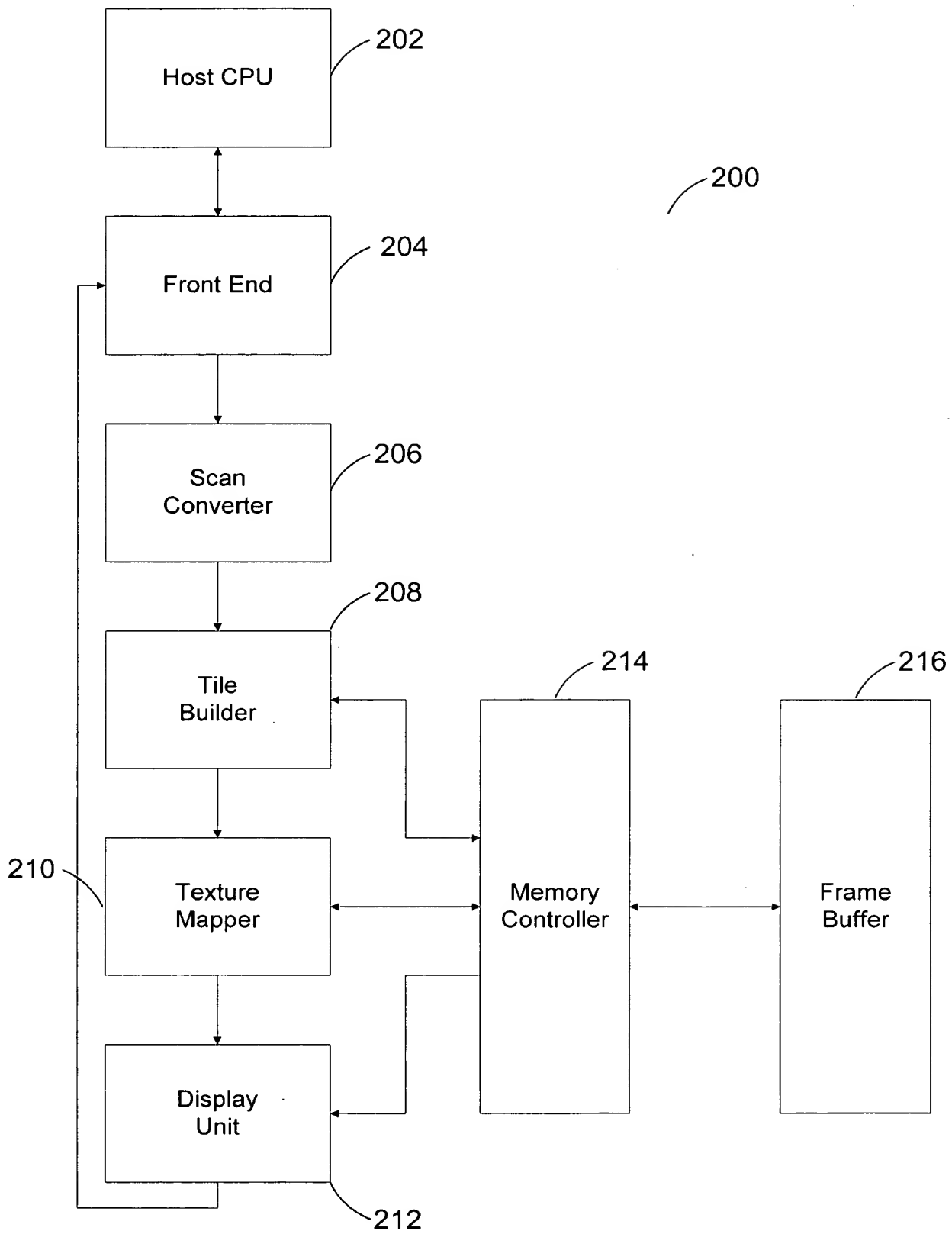


Fig. 2

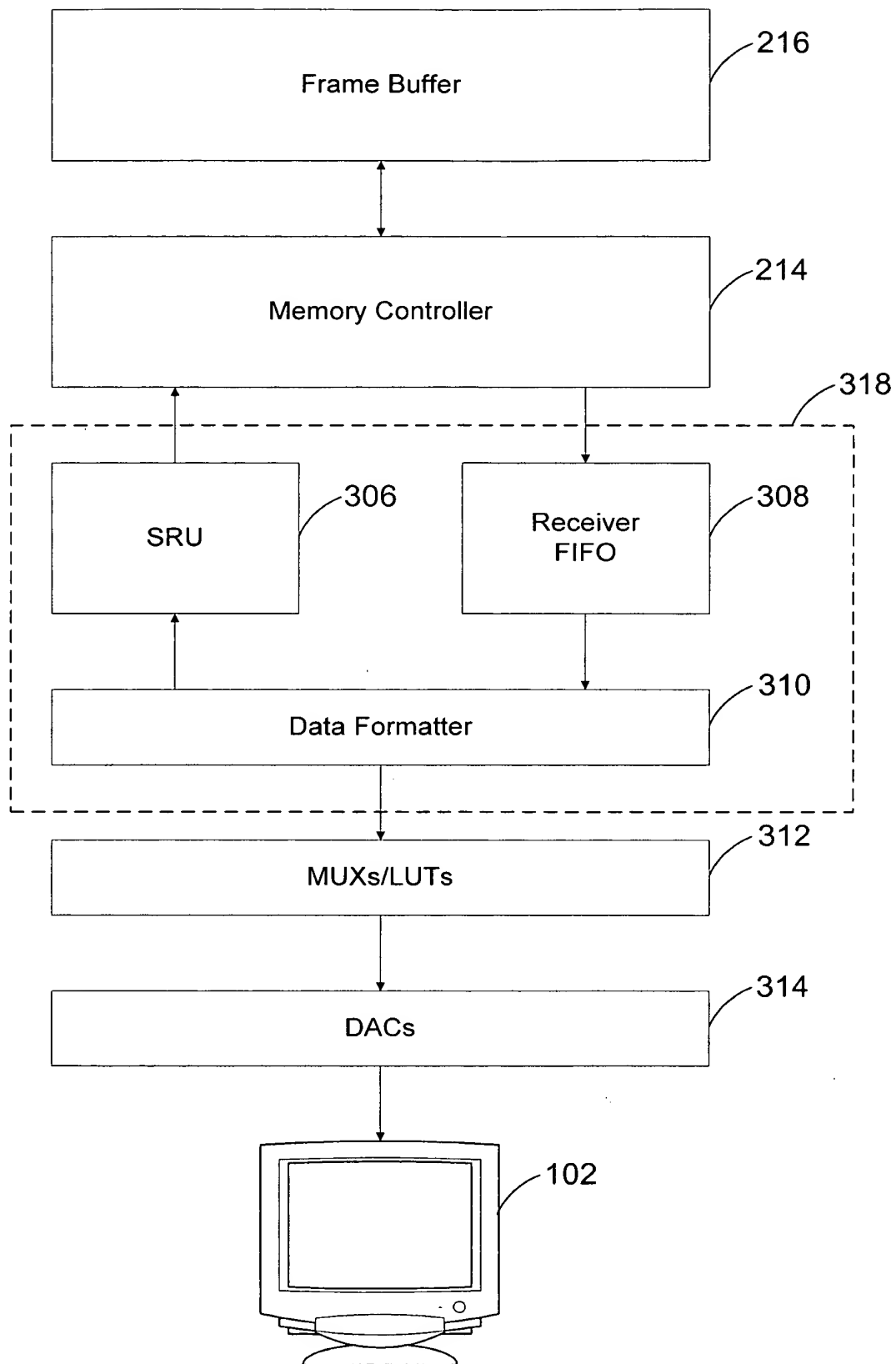


Fig. 3

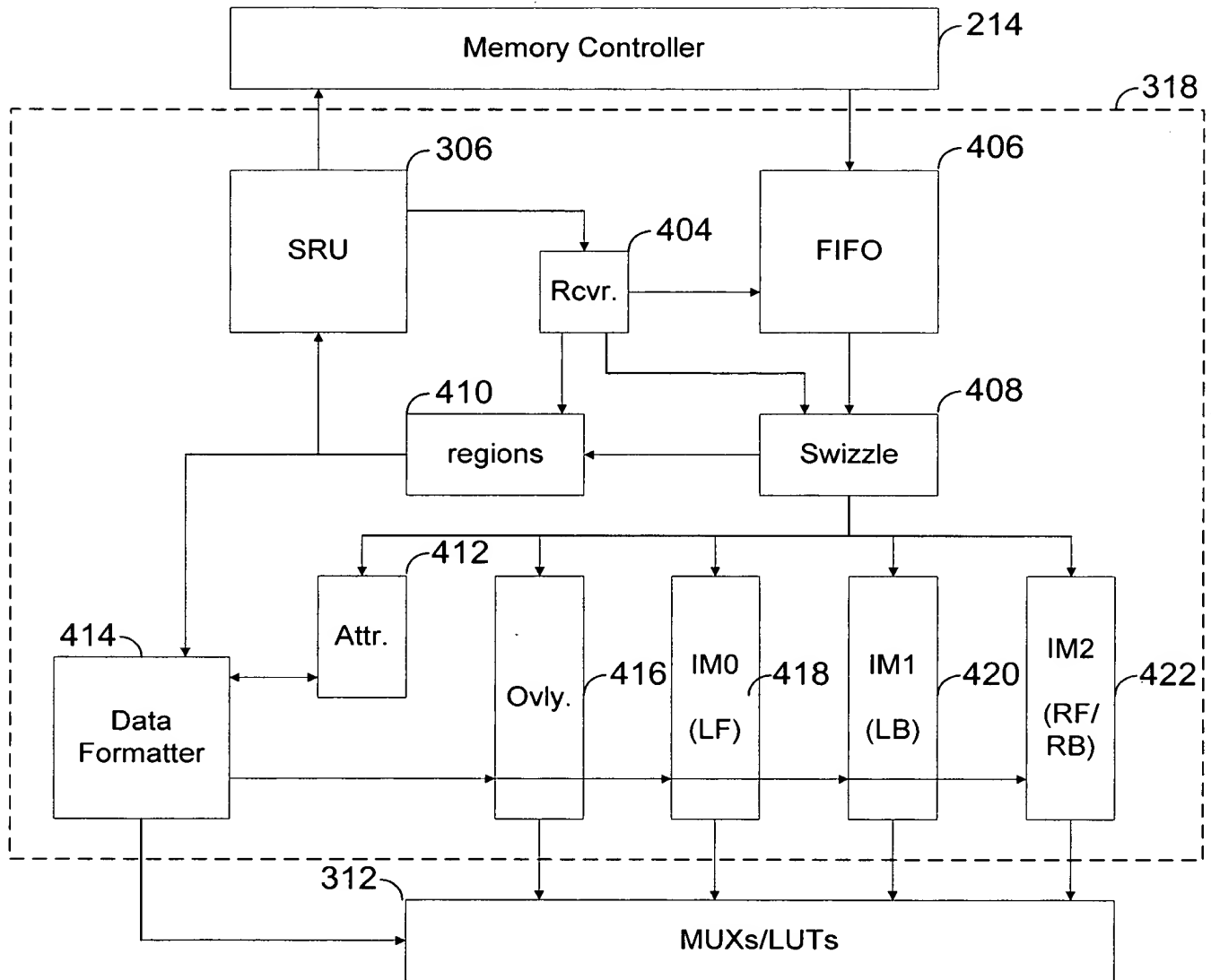


Fig. 4

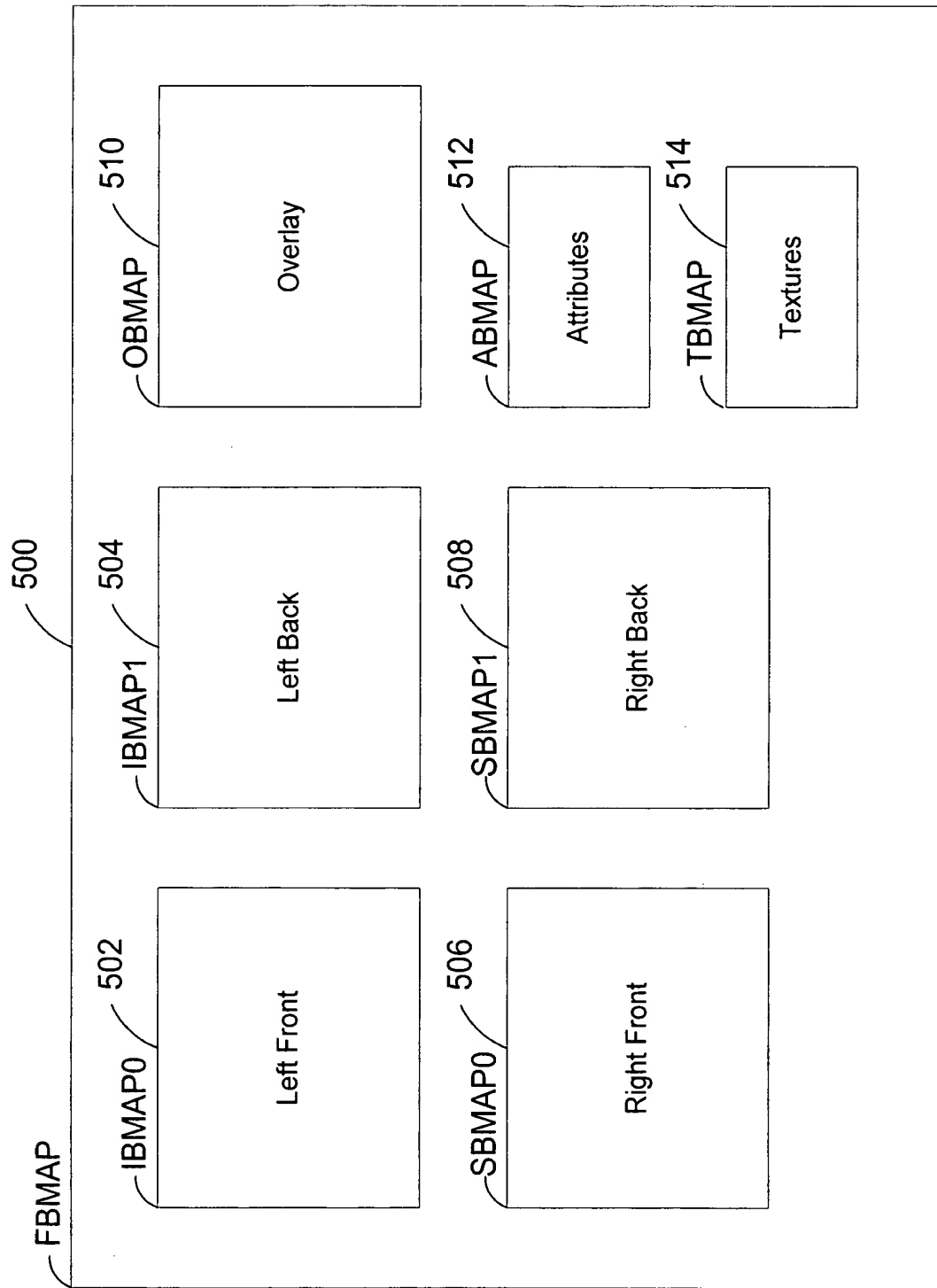


Fig. 5

Figure 1 is a schematic diagram of a display system. The diagram is divided into two main sections by a vertical dashed line. On the left, labeled "DISPLAY", is a monitor (102) with a screen (602) containing a central rectangle (604). On the right, labeled "FRAME BUFFER ATTRIBUTE DATA", is a data structure (512) containing a grid of bits. The top row of bits is 0000000000000000. The second row is 0000000000000000. The third row is 0000000000000000. The fourth row is 00000000111111. The fifth row is 00000000111111. A dashed line connects the display area to the data area, with a label 608 pointing to the data area.

Fig. 6

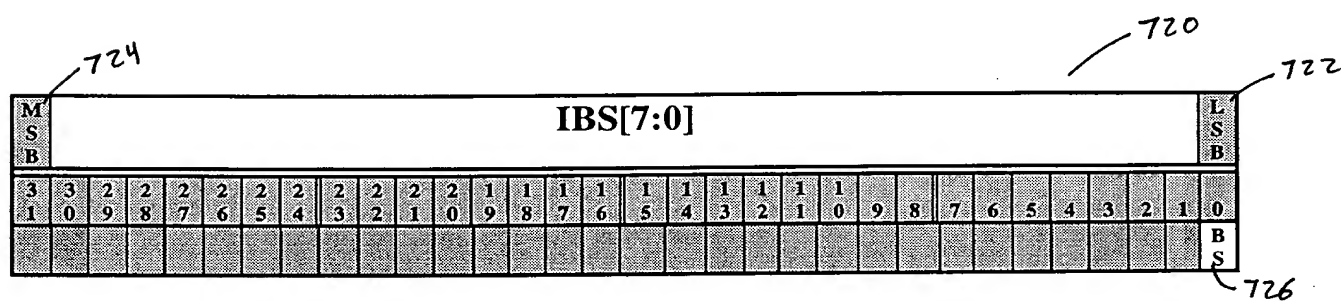
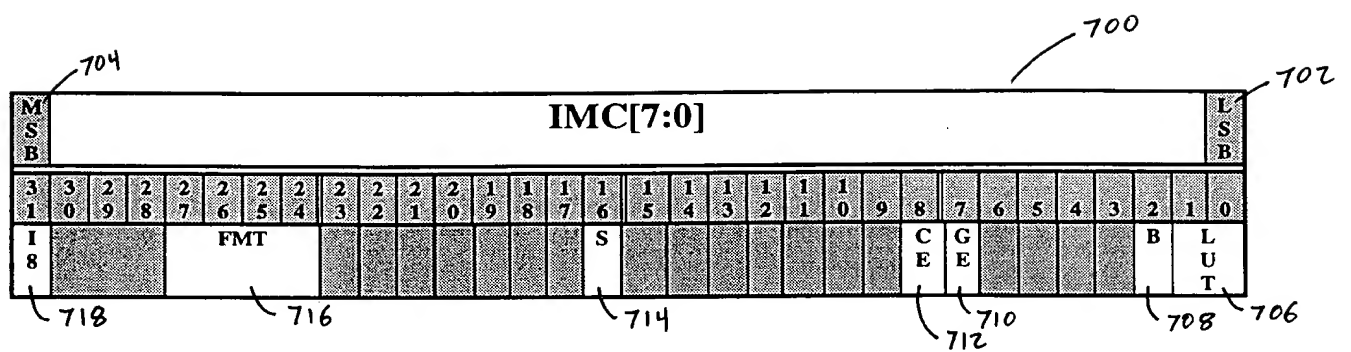


FIG. 7